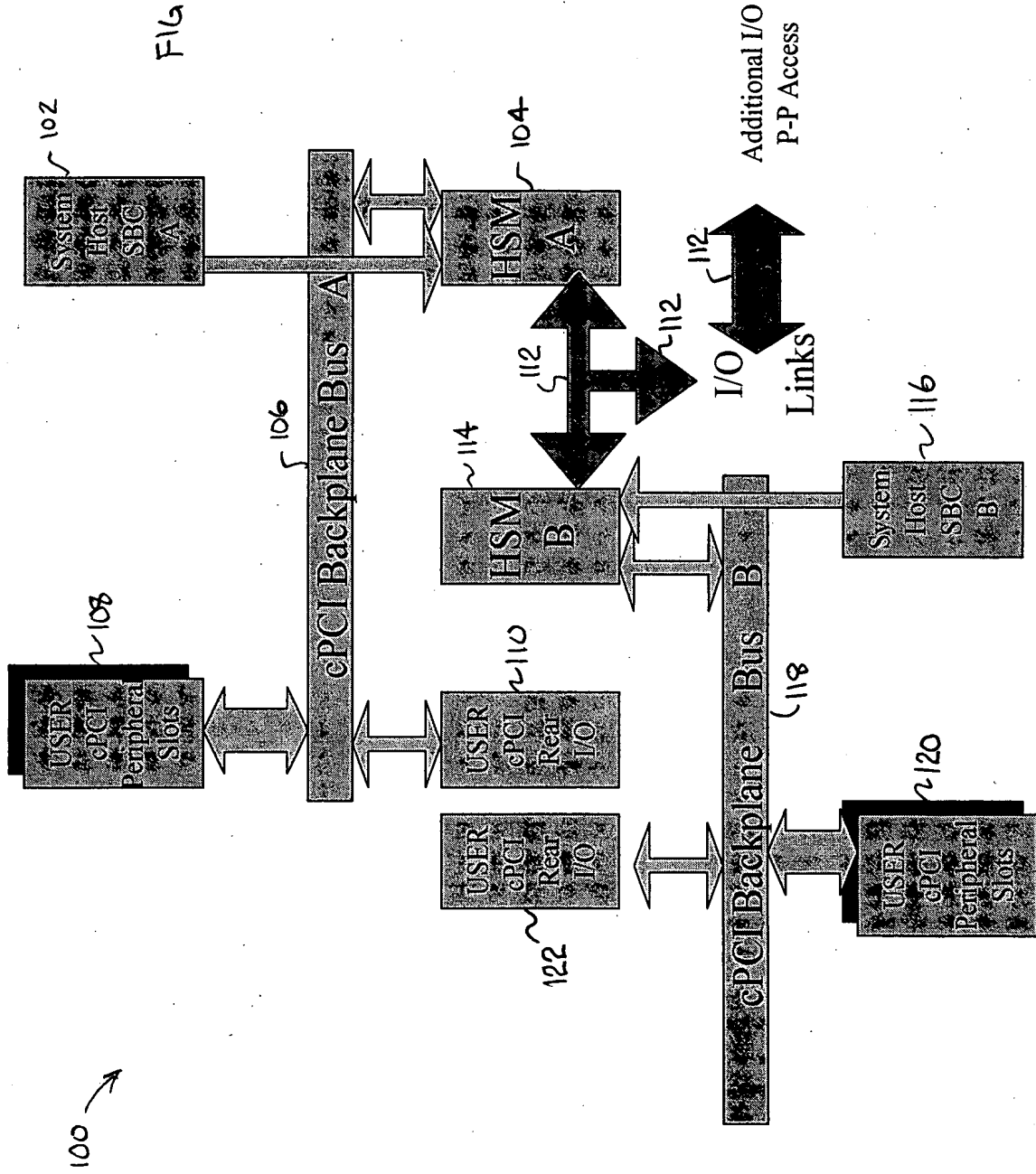


# BACKPLANE CONNECTIONS

FIG. 1



# cPCI SIGNALS ROUTING

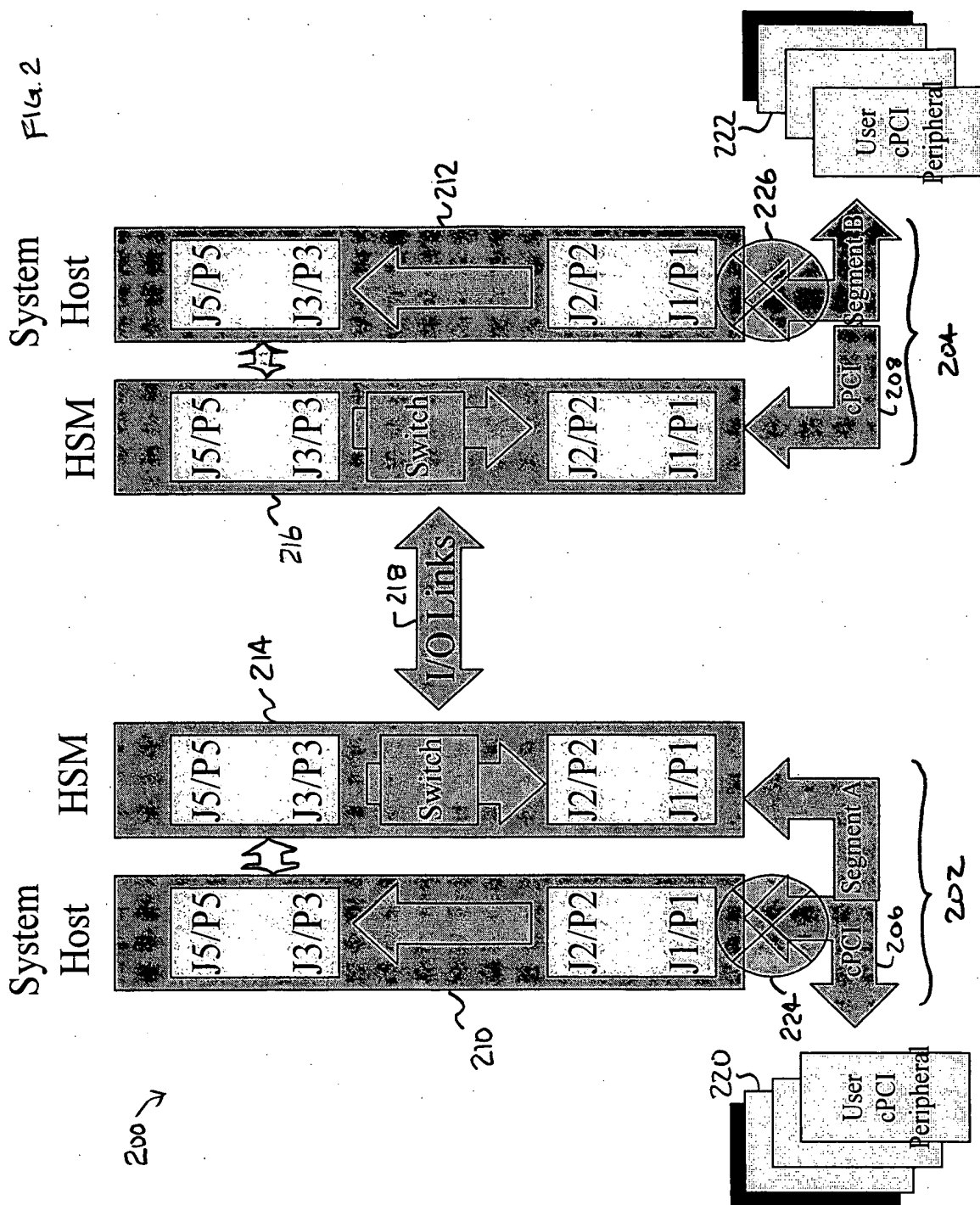


FIG. 3

# BACKPLANE DESIGN

FIG. 3

300 →

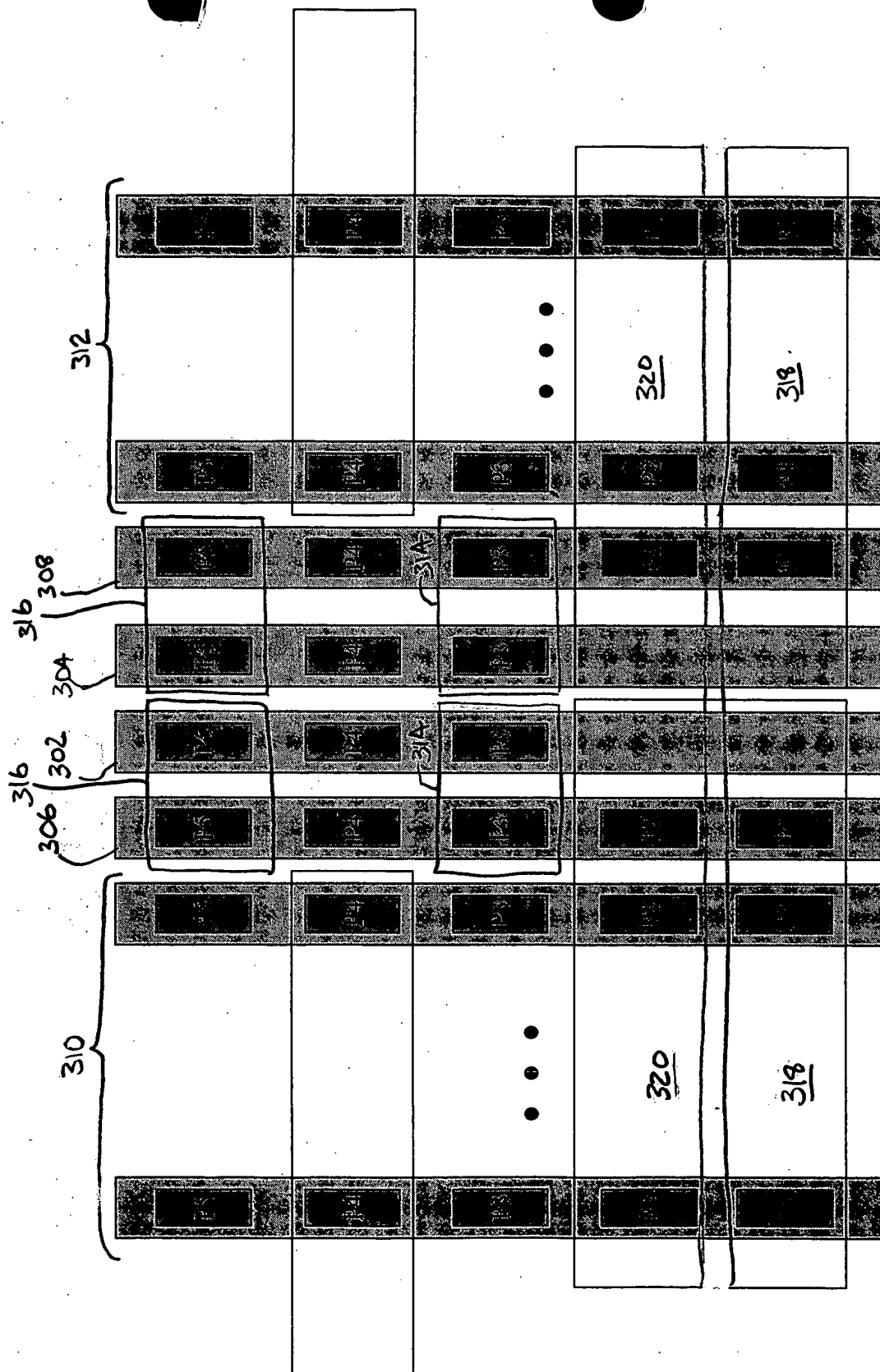


FIG. 4

# Hybrid Switching Module

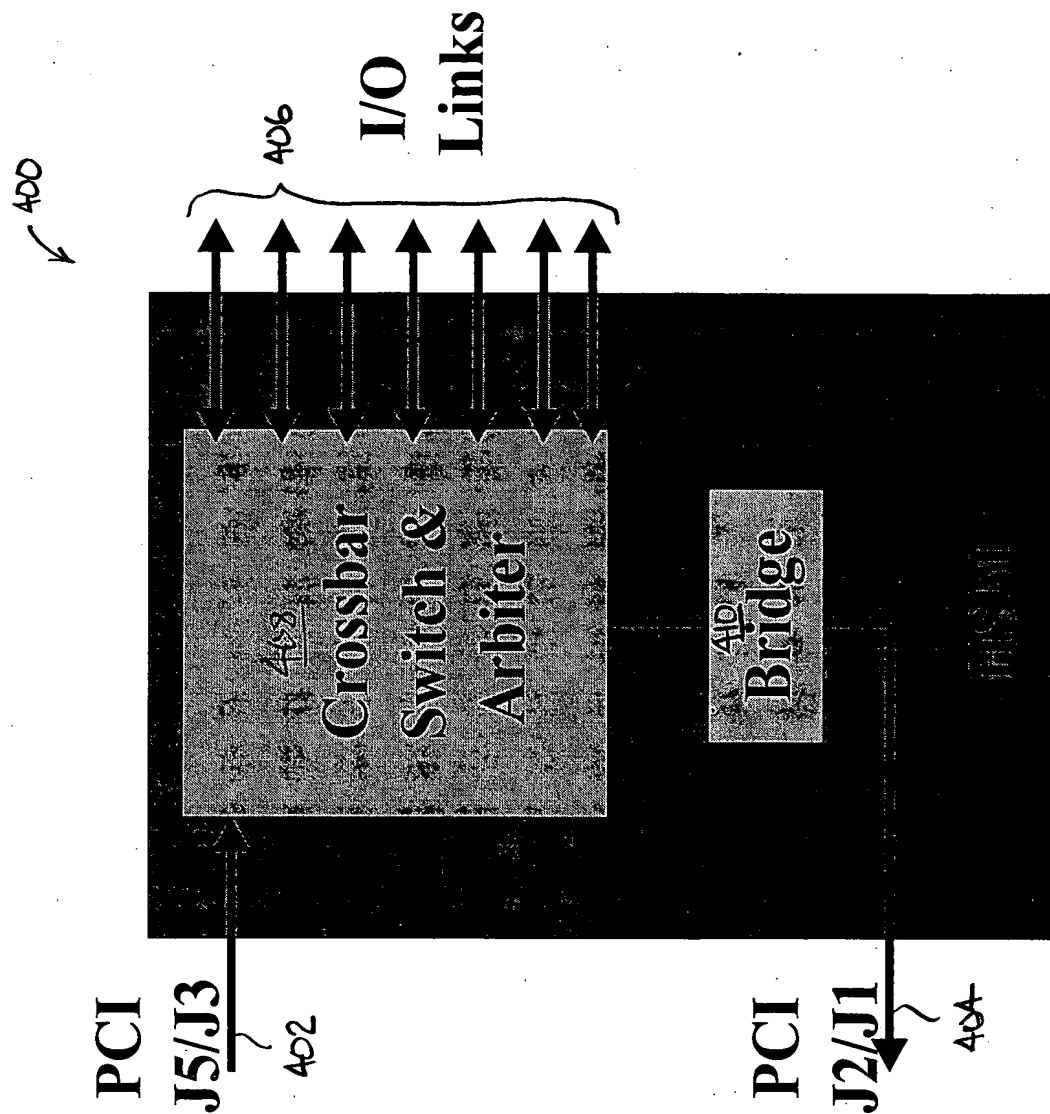


FIG. 5

# PCI-IB Migration

